microprocessors and microsystems

Index to Volume 6, Number 1–10, Pages 1–560 (1982)

Subject Index

Architecture
  Serial 117
Automotive electronics
  drive-by-wire steering 361
  engine control microcomputer 347
  ignition advance control 355
  instrumentation 367, 371, 378
  survey 381

Bleasdale
  600 16-bit microcomputer 157
Book reviews
  Anderson, T Fault tolerance: principles and practice 35
  Artwick, B A Microcomputer interfacing 33
  ASTMS health and safety monitor 9 Prevention of occupational eye-strain – hazards of VDUs 198
  Banerji, D K Elements of microprogramming 436
  Bibbero, J Microprocessor systems, interfacing and applications 437
  Brodie, L Starting FORTH 145
  Bywater, R E H Hardware/software design of digital systems 143
  Cahill, S J Digital and microprocessor engineering 435
  Chamberlain, H Musical applications of microprocessors 322
  Cirovic, M Handbook of semiconductor memories 322
  Cline, B E Microprogramming – concepts and techniques 435
  Council for Science and Society New technology: society, employment and skill 198
  Goldsbrough, P F Microcomputer interfacing with the 8255 PPI chip 33
  Greenfield, J D Using microprocessors and microcomputers: the 6800 family 142
  Hope, G Integrated devices in digital circuit designs 144
  Katz, P Digital control using microprocessors 323
  Kneen, J Logic analysers for microprocessors 437
  Knight, K Introduction to Forth 438
  Manpower Services Commission A framework for the future: a sector by sector review of industrial and commercial training 198
  Mars, P Stochastic and deterministic averaging processors 143
  Popplebaum, W J – see **Mars, P** 143
  Pyle, I C The ADA programming language reference manual 199
  Raymond, J – see **Banerji, D K** 436
  Roberts, S K Industrial design with microcomputers 438
  Smith, N T An introduction to microprocessors 323
  Staugaard, A C 6809 microcomputer programming and interfacing with experiments 34
  Stern, D M – see **Bibbero, J** 437
  Tseng, V Microprocessor development and development systems 434
  Witten, I H Communicating with microcomputers 33
  Wray, W C see **Greenfield, J D** 142
Backplane bus standards 450
  Eurobus 483
  Futurebus 489
  Multibus 471, 505
  S100 467, 504
  STD 455
  VERSAbus 475
  VMEbus 475
Bus standards – see Backplane bus standards

Chip count
  minimization 239
Company profiles
  AIM Cambridge 255
  Transdata 197
  3D 318
Conferences
  British Microcomputer Manufacturers' Group Seminar 150
  Info '82 151
  NCC 310
Control
– see also Process Control
  boiler temperature 247
  data link 9
  domestic heating 235, 250
  drilling machine – retrofitting 287
  furnace temperature 225
  internal combustion engine 347, 355
  peripherals 107
  solar energy plants 535
  6802 control of field strength analyser 177

Data acquisition
  logic signals 69
DEC single chip PDP-11 395
Development systems 555
  HP 64000 emulation 79
  low cost entry 98
  market report
  network 21
Dragon 32 330

Education
  real-time programming 529
  serial architecture 117
  video on computers and society 338
Emulation
  HP 64000 development system 79
Energy conservation
  microprocessor techniques in computer rooms 219
Environmental control
  boiler control 247
  maximum demand 246
  microprocessor techniques in computer tooms 219
  optimum start 246
  solar energy 535
  survey 248
Ethernet
  designer's guide 405
  manufacturers 29
  marketing 334
  network controller 440
  specification 26
Exhibitions
  British Toy Hobby Fair 152
  MicroCity '82 317
  NCC 315
  Which Computer? Show 80
Expansion
  I/O channels 189
  memory of SDK-86 77

Ferranti
  CAD Designer 88
  in Hong Kong 554
Floppy discs
  market 398
  reduced height 394
  3½-in standards 510
Fujitsu
  Micro 8 442

Hewlett-Packard
  32-bit micro 166
  64000 development system emulation 79

IEEE-488
unit as a development aid 275
using parallel I/O ports 281
Initialization
I/O channels 189
Intel
iAPX 186 330
iAPX 286 153, 204
iAPX 432 440
SDK-86 77
Interactive software
process structure 297

Locks 340
Logica personal computer 547
Lynx 505

Market reports
Business software (world) 50
CMOS replacing NMOS 399
Computer games (world) 265, 508
Data acquisition (Europe) 447
Database management systems (US) 103
Development systems (world) 555
Floppy discs (world) 398
Hotel communications 448
IC sales (Europe) 553
Industrial robots 552
Local area networks (US) 210
Medical image processing (world) 210
Microcomputer sales (world) 211
Microprocessor (Europe) 265
OCRs in the home 553
Office automation (US) 53
PCBs (Europe) 508
Portable computers (world) 100
Robots (Europe) 264, 333
Security/fire/energy (Europe) 335
Semiconductor market (Europe) 49
Semiconductor sales (world) 336
Semiconductor production (Japan) 335
Small computer market (world) 165
Speech synthesis (world) 49
Storage media (world) 165
'Supermicro' market (world) 164
Voice digitizers (world) 448
Memory
expansion of SDK-86 77
Microprofessor II 395
Monitoring of EC-1030 183
MODULA-2 297
Motorola
6800-based meetings system 135
6800-based monitoring 183
6802-based analyser 177
68000 support chips 258
simulation of 68000 131
Multimicroprocessors
communication through common memory 303
network simulator 15

National Semiconductor
16032 emulator 393
16000 series 328
Networks 430
access unit 3
baseband 27
broadband 27
Brown University – BRUNET 413
CASE 329
definition 25
development system 21
Ethernet –see Ethernet
IC data link controller 9
in manufacturing 425
IPA processing architecture 171
meetings system 135
Net/One 419
NEWTS 15
PABX 59
product survey 27
simulation 15
standardization 171
Strathnet 3, 21
switching 59
topology 26
US market forecast 210
Z-NET II 256
NewBrain 267
Numerical control 519
retrofitting for drilling 287
software for two axis system 293

OCR 339
Operating systems
CP/M and MS-DOS upgrades 556
Oric I 505
Osborne 1 155

PABX
switching 59
PASCAL
development 506
Process control
and monitoring microcomputer 446
beer production 445, 343
fungicide 343
knitting 342
Programming
teaching 529

Research report
Intotech 320

Sage II 256
Simulation
time delay 73
network 15
machine cycle 131
Sirius 1 257
Sony
3½-in floppy standard 510
Special issue
Automotive electronics 345–400
Backplane bus standards 449–512
Environmental control 217–272
Local area networks 401–448
Speech synthesis
portable speech lab 327
Standards
backplane buses 449–512
ISO network reference model 171
report 544

Televideo microcomputers
TS816 440
Texas Instruments
portable speech lab 327
99000 40

UNIX
on Codata CTS and CTW 396
on Zilog micros 503

Video Genie
CP/Genie 442

Westminster report
'Automate or liquidate' – Kenneth Baker 195
Government investment in new technology decided for 1985 433
IT '82 advances on broad front 149
UK reply to Japanese fifth generation project 543
£60M goes to engineering automation 309

Zilog
supermicros 503
Z-NET II 256

Title Index

An 8-bit single-chip microcomputer for automotive engine control 347
A gateway development system 21
An IEEE-488 interface using parallel I/O ports 281
A low cost entry into development systems 98
A microprocessor-based local network access unit 3
A parallel processing simulator for a network system using multimicroprocessors 15
Applying microcomputers to vehicle instrumentation 371
'Automate or liquidate' – Kenneth Baker 195
Backplane bus standards – editorial 450
Brown University network –BRUNET 413
Circuit idea for expanding the 8086-based single-board computer (Design note) 77
Communication between two microprocessors through common memory 303
Computer-controlled model railway for use in teaching real-time programming 529
Design concept for a microprocessor-based temperature controller 225
Design of a microcomputer-based meetings system (Design note) 135
Design of a microprocessor-controlled PABX 59
Design of a 6802-controlled propagation field strength analyser for land mobile radio services 177
Developing a microprocessor-based energy management controller 235
Ethernet designer's guide 405
Eurobus – a UK-designed general-purpose backplane bus 483
Expansion and initialization of serial I/O channels (Design note) 189
Experimenting with local networks using an integrated circuit data link controller 9
Government investment in new technology decided for 1985 433
Highly serial architecture for a microprocessor 117
IEEE P896 – the Futurebus project 489
Implementation of local area

networks 430
Interconnection services of Net/one 419
IPA — a processing architecture based on the ISO network reference model 171
IT '82 advances on broad front 149
Knowledge and experience-based systems for analysis and design of microprocessor applications hardware 513
Local area networks 25
Local area networks —editorial 403
Machine cycle simulator for a microprocessor 131
Major standardization issues of the proposed IEEE 796 bus — Multibus 471
Microprocessor-based IEEE-488 interfacing unit as a development aid 275
Microprocessor-based simulator of a variable time delay 73
Microcomputer-controlled solar energy plants 535
Microprocessor control of ignition advance angle 355
Microprocessor control of multiaxes continuous path motion 519
Microprocessor techniques for energy conservation in computer rooms 219
Microprogrammable peripheral controller 107
Miniature printer for automotive applications (Design note) 378
Minimizing chip count in a microsystem 239
Noncontact test signal acquisition 69
Performance monitoring of the EC-1039 system by a microprocessor-based monitor 183
Process structure for interactive software 297
P896/S100 — a bus which supports a wide range of 8- and 16-bit processors 467
Reliable microprocessor control for drive-by-wire car steering for the disabled 361
Single-chip microcomputer for use in automotive instrumentation 367
Software for a simple numerical control system 293
Standard bus for 8-bit microprocessor systems 455
£60M goes to engineering automation 309
6802/6808 emulation for the HP 64000 microprocessor development system 79
The computerized family car will be commonplace in Europe before the end of the decade 381
The use of microelectronics in energy conservation and control 245
UK government venture into standards 544
UK reply to Japanese fifth generation project 543
Use of local area networks within manufacturing systems 425
VERSAbus — a multiprocessor bus standard — and VMEbus — its Eurocard counterpart 475

Author Index

Abdel-Fattah, A see Kamal, A A 59
Abou-Kandil, H A see Kamal, A A 59
Al-Riahi, H Design of microcomputer-based meetings system 135
Augustine, A see Veerabhadraiah, S 77
Bass, C Local area networks 403
Bhot, S Microprocessor control of ignition advance angle 355
Birch, B Applying microcomputers to vehicle instrumentation 371
Boberg, R Major standardization issues of the proposed IEEE 796 bus — Multibus 471
Borrill, P Backplane bus standards — editorial 450
Borrill, P IEEE P896 — the Futurebus project 489
Bowen, J A see Smith, M F 515
Campbell, B An IEEE-488 interface using parallel I/O ports 281
Cheah, Y C Design of a 6802-controlled propagation field strength analyser for land mobile radio services 177
Corcoran, P see Hutchison, D 3
Corcoran, P see Shepherd, D 21
Crane, R Ethernet designers guide 405
Daneshmend, L K see Pak, H A 519
Davidson, J M Interconnection services of Net/one 419
DeBock, R VERSAbus — a multiprocessor bus standard — and VMEbus — its Eurocard counterpart 475
Dowsing, R Computer-controlled model railway for use in teaching real-time programming 529
Elsmore, T Standard bus for 8-bit microprocessor systems 455
Ettinger, G M Noncontact test signal acquisition 69
Faro, A Microcomputer-controlled solar energy plants 535
Garetz, M P696/S100 — a bus which supports a wide range of 8- and 16-bit processors 467
Green, D The computerized family car will be commonplace in Europe before the end of the decade 381
Gwinn, R see Rolls, T 225
Hanlon, P D Use of local area networks within manufacturing systems 425
Hart, C Miniature printer for automotive applications (Design note) 378
Hatada, M see Toda, A 15
Haynes, N A Reliable microprocessor control for drive-by-wire car steering for the disabled 361
Hill, J Eurobus — a UK-designed general-purpose backplane bus 483
Hinsley, R S Microprocessor techniques for energy conservation in computer rooms 219
Hiyama, K see Toda, A 15
Hoffner, Y Communication between two microprocessors through common memory 303
Hutchinson, M A Microprocessor-based local network access unit 3
Imai, M see Toda, A 15
Inamori, H see Toda, A 15
Ionnides, A C see Majoe, D P 275
Iwasaki, J see Katori, S 347
Jones, G see Monk, J 117
Kamal, A A Design of a microprocessor-controlled PABX 59
Katori, S An 8-bit single chip microcomputer for automotive engine control 347
Kemp, J IPA — a processing architecture based on the ISO network reference model 171
King, P A low cost entry into development systems 98
Kirshnan, A see Santhanam, P 293
Lanigan, M see Al-Riahi, H 135
Loader, R J see Smith, M F 9
Maehashi, Y see Katori, S 347
Majoe, D P Microprocessor-based IEEE-488 interfacing as a development aid 275
Martin, A G see Haynes, N A 361
Mathew, J see Veerabhadraiah, S 77
Messina, G see Faro, A 535
Metwally, M S see Kamal, A A 59
Middelboe, S Local area networks 25
Middelboe, S The use of microelectronics in energy conservation and control 245
Mirabella, O see Faro, A 535
Monk, J Highly serial architecture for a microprocessor 117
Moore, W P see Haynes, N A 361
Moores, W Implementation of local area networks 430
Morgan, L UK government venture into standards 544
Muller, R Single-chip microcomputer for use in automotive instrumentation 345
Ogden, J Process structure for interactive software 297
Pak, H A Microprocessor control of multiaxes continuous path motion 519
Panchapakesan, S see Santhanam, P 293
Pradhan, T see Rao, S S S P 107
Quayle, R see Bhot, S 355
Radhakrishnan, L S see Rao, S S S P 183
Rao, S S S P Microprogrammable peripheral controller 107
Rao, S S S P Performance monitoring of the EC-1030 system by a microprocessor-based monitor 183
Reynolds, R see Kemp, J
Roberts, G 'Automate or liquidate' — Kenneth Baker 195
Roberts, G Government investment in new technology decided for 1985 433
Roberts, G IT '82 advances on broad front 149
Roberts, G £60M goes to engineering automation 309
Roberts, G UK reply to Japanese fifth generation report 543
Robertson, P see Campbell, B 281
Rolls, T Design concept for a microprocessor-based temperature controller 225
Santhanam, P Software for a simple numerical control system 293
Saward, P see Dowsing, R 529
Shepherd, D A gateway development system 21
Shipp, W Brown University network — BRUNET 413
Shooter, L T see Ettinger, G M 69
Shukla, S V see Rao, S S S P 183
Smith, M F Experimenting with local networks using an inte-

grated circuit data link controller 9
Smith, M F Knowledge and experience-based systems for analysis and design of microprocessor applications hardware 515
Smith, M F 6802/6808 emulation for the HP 64000 microprocessor development system (Design note) 79
Smith, M F see Hoffner, Y 303
Stajić, D Microprocessor-based simulator of a variable time delay 73
Stewart, A J see Williams, M H 131
Tillier, M see Ettinger, G M 69
Toda, A A parallel processing simulator for a network system using multimicroprocessors 15
Vaidya, D M Expansion and initialization of serial I/O channels (Design note) 189
Vaidya, D M Minimizing chip count in a microsystem 239
Veerabhadraiah, S Circuit idea for expanding the 8086-based single-board computer (Design note) 77
Waddington, T Developing a microprocessor-based energy management controller 235
Webster, H see Shipp, W 413
Weston, R H see Hanlon, P D 425
Williams, M H Machine cycle simulator for a microprocessor 131
Willis, A see Rolls, T 225